Criterion 2 – Teaching Learning and Evaluation

Key Indicator 2.3 – Teaching –Leaning Process

2.3.1. Student centric methods, such as experiential learning, participative learning and problem solving methodologies are used for enhancing learning experiences using ICT tools

INDEX

GURU NANAK DEV ENGINEERING COLLEGE BIDAR

Department of Electronics and Communication Engineering

Innovative Case Study: Exploring the 8051 Microcontroller and Microprocessor

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Faculty Name: Dr. Veerendra D(Associate Professor)

SUBJECT: 8051 Microcontroller (18EC46)

Date of Conduction: 18-02-2020 Time: 4pm-5pm

Semester: 4th Semester.

Title: Innovative Case Study: Exploring the 8051 Microcontroller and Microprocessor

1. Introduction:

In this innovative case study, students were engaged in an interactive learning experience focused on the 8051 microcontroller and microprocessor. The objective was to enhance their understanding, analytical skills, and communication abilities through group presentations. By dividing the students into three groups based on their academic standards, a balanced learning environment was created.

The 8051 microcontroller is a widely used and versatile device in the field of embedded systems. It has found its application in various domains such as consumer electronics, industrial automation, and robotics. In this case study or group discussion, we will delve into the internal architecture and memory organization of the 8051 microcontroller. This innovative teaching and learning method aimed to provide a comprehensive understanding of the topic through active participation and collaborative learning.

2. Objective:

The primary objective of this case study or group discussion is to enhance participants' knowledge and understanding of the 8051 microcontroller's internal architecture and memory organization. By actively engaging in the discussion, participants will gain insights into the key components, their functions, and how they interact with each other. The case study will also

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encourage participants to think critically, analyze real-world scenarios, and apply their knowledge to practical applications.

Group Assignments:

Group 1: Internal Architecture Presentation:

Group 1 was tasked with presenting on the internal architecture of the 8051 microcontroller. They delved into the components, functions, and interactions within the microcontroller's architecture. Through research and collaborative discussions, they acquired an in-depth understanding of the intricate design of the microcontroller.

Group 2: Pros and Cons of Microcontroller and Microprocessor:

Group 2 was given the responsibility of presenting the advantages and disadvantages of microcontrollers and microprocessors. They explored the distinctive features and applications of both, highlighting their strengths and limitations. The group's comprehensive analysis helped the students gain insights into choosing the appropriate technology for different scenarios.

Group 3: Features and Applications Presentation:

Group 3 focused on presenting the various features and applications of microcontrollers and microprocessors. They showcased real-world examples where these technologies play a vital role, such as embedded systems, robotics, and consumer electronics. The group's presentation encouraged students to envision practical applications and fostered innovation.

3. Methodology:

3.1 Preparatory Phase:

a. We provided participants with relevant study materials, including textbooks, online resources, datasheets, and technical documents related to the 8051 microcontroller.

b. Encouraged participants (students) to conduct preliminary study and come prepared with questions, examples, and real-life applications of the 8051 microcontroller.

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Rules:

1. The students were divided into three groups, ensuring a balance in their academic standards.
2. **Group 1** was assigned the task of presenting on the internal architecture of the 8051 microcontroller.
3. **Group 2** was given the task of presenting the pros and cons of microcontrollers and microprocessors.
4. **Group 3** was assigned the task of presenting the features and applications of microcontrollers and microprocessors.

This activity enhances the students' understanding and analysis of the topic compared to the traditional chalk and talk method. It helps them remember the concepts clearly without confusion. Additionally, this activity assists students in overcoming stage fear, improving communication skills, analytical abilities, leadership qualities, teamwork, and innovation skills.

The beauty of this brainstorming style is its flexibility to adapt to the team's needs. Various mediums, such as pen and paper, whiteboards, or Post-Its, can be used to facilitate creative thinking. The time limit for the rapid ideation session can range from five to 45 minutes.

While brainstorming serves as the foundation for developing ideas and encouraging students to generate new concepts and solutions, there is a tendency to overthink things. It's easy to get caught up in every new idea and its intricacies. Rapid ideation, however, aims to address this issue by promoting quick thinking and idea generation.

PRESENTATION GROUPS			
SL.NO	**GROUP 1: REPRESENTATIVES**	**GROUP 2: REPRESENTATIVES**	**GROUP 3: REPRESENTATIVES**
1	ADITYA	AISHWARYA	GADGI VISHAL
2	AMEETH P	DIVYA	VINAY KUMAR

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Group-1:

Aditya and Ameeth took charge of presenting the internal structure of the 8051 microcontroller. They provided a comprehensive overview of the various components and their functions within the microcontroller. As they delivered their presentation, the remaining students actively participated by asking questions and engaging in discussions to further analyze and understand the complete internal structure of the 8051 microcontroller.

Group-2:

Aishwarya and Divya were responsible for presenting the pros and cons of microcontrollers and microprocessors. They skillfully outlined the advantages and disadvantages of both technologies, highlighting their respective strengths and limitations. As they presented, the other students actively participated by posing questions and engaging in discussions to analyze and comprehend the differences between microcontrollers and microprocessors.

Group-3:

Vishal, Vinay Kumar, and Ameeth collaborated to present the features and applications of microcontrollers and microprocessors. They showcased the diverse range of features and demonstrated how these technologies are applied in various domains. Simultaneously, the remaining students contributed by asking questions, fostering discussions, and analyzing the features and applications of microcontrollers and microprocessors to gain a deeper understanding.

Throughout the presentations of Group-1, Group-2, and Group-3, all students actively participated in the discussions, asked pertinent questions, and engaged in critical analysis. This collaborative approach allowed for a comprehensive exploration of the topics, facilitating a deeper understanding of the internal structure, pros and cons, as well as the features and applications of microcontrollers and microprocessors.

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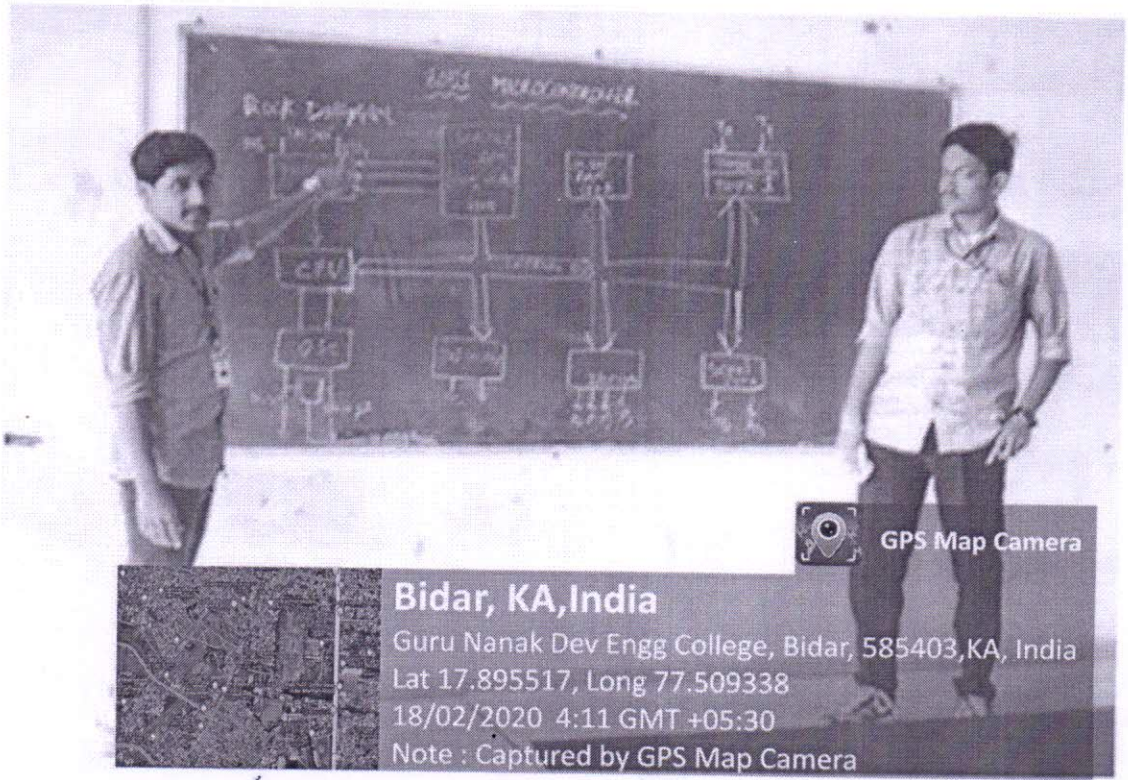
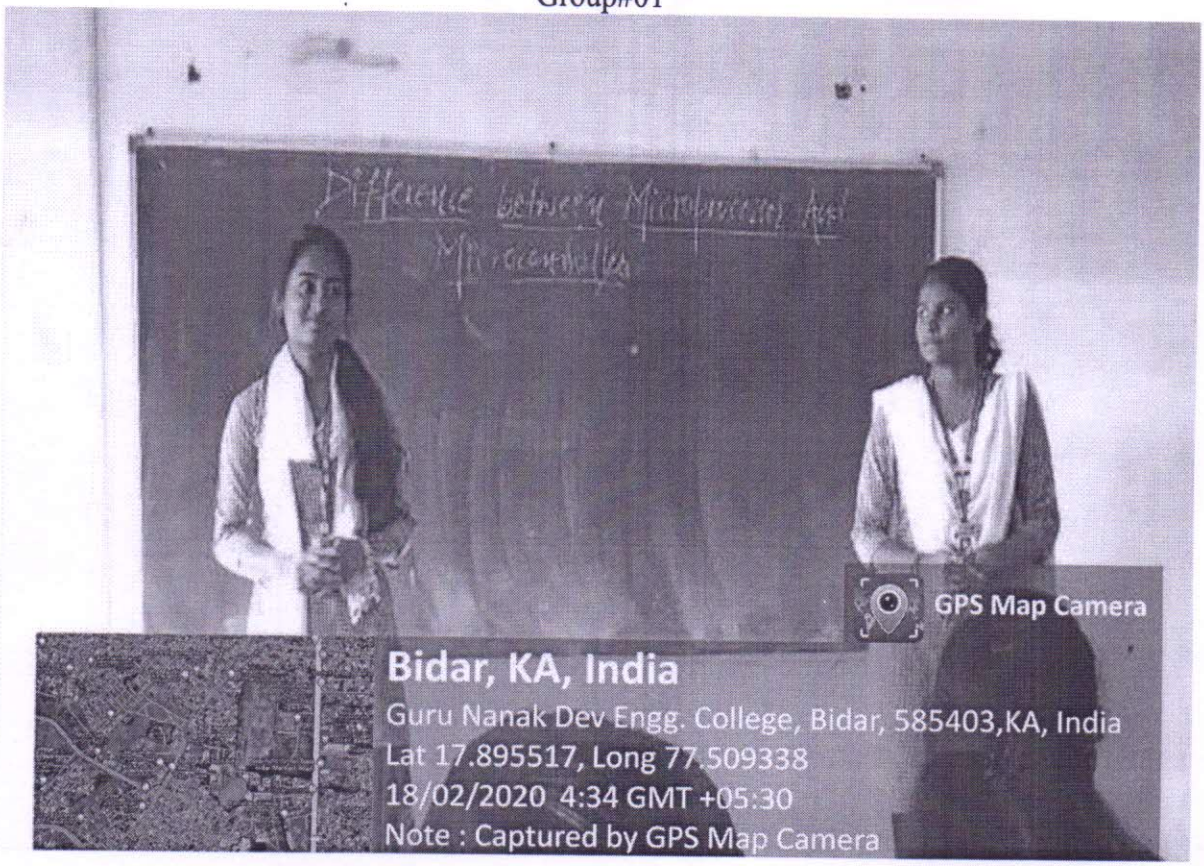


Group#01



Group#02

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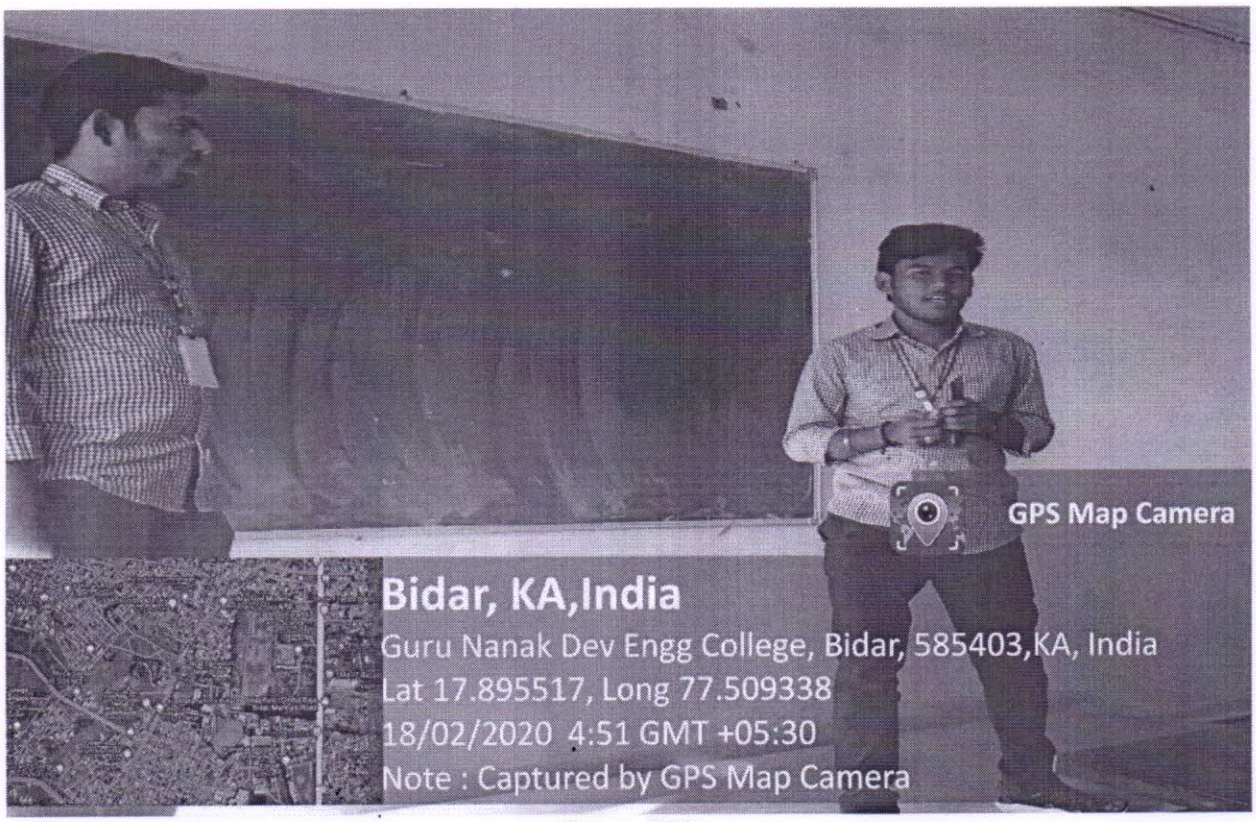

Group#03

3.2 Case Study

a. Introduction (10 minutes):

We started with a brief overview of the 8051 microcontroller, its significance, and its applications. Highlighted the importance of understanding the internal architecture and memory organization for effective programming and system design.

b. Internal Architecture (20 minutes):

We have divided participants into small groups and assigned each group a specific component of the 8051 microcontroller's internal architecture, such as the ALU (Arithmetic Logic Unit), CPU (Central Processing Unit), Register Banks, Timers/Counters, Interrupts, etc.

Each group presented their findings to the rest of the participants, explained the purpose, functionality, and interaction of their assigned component with other parts of the microcontroller.

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Encourage participants to ask questions, seek clarification, and engage in discussions to foster a deeper understanding.

c. Memory Organization (10 minutes):

Repeated the grouping process and assigned each group a specific type of memory in the 8051 microcontroller, such as ROM (Read-Only Memory), RAM (Random Access Memory), Special Function Registers (SFRs), etc. Each group discussed their assigned memory type, its purpose, organization, addressing modes, and usage in various applications. Facilitated cross-group discussions to compare and contrast the different memory types and their significance in programming and system design.

d. Real-life Scenarios and Applications (20 minutes):

Presented participants with real-life scenarios or applications that require an understanding of the 8051 microcontroller's internal architecture and memory organization.

Engage participants in brainstorming sessions, where they analyze the requirements, identify the relevant components and memory types, and propose possible solutions or implementations.

Encouraged participants to consider trade-offs, limitations, and design considerations while discussing their proposed solutions.

5. Outcomes

Active Learning: This case study or group discussion approach promotes active learning, as participants actively engage in discussions, presentations, and problem-solving activities. It encourages critical thinking, analysis, and application of knowledge to real-life scenarios.

Collaborative Learning: By dividing participants into small groups, the case study fosters collaboration, teamwork, and peer learning. Participants can learn from each other's perspectives, experiences, and insights, leading to a richer and more comprehensive understanding of the topic.

Practical Application: The focus on real-life scenarios and applications allows participants to see the direct relevance of the 8051 microcontroller's internal architecture and memory

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organization. It enables them to bridge the gap between theoretical knowledge and practical implementation.

Enhanced Retention: Actively participating in discussions, presenting findings, and proposing solutions helps participants reinforce their understanding and retain the information for longer periods. The case study format promotes deeper learning and retention of concepts.

Holistic Understanding: By covering both the internal architecture and memory organization of the 8051 microcontroller, participants gain a holistic understanding of the device. They learn how different components interact with each other and how memory is organized and utilized in programming and system design.

6. Conclusion:

The innovative case study approach employed in this learning session on the 8051 microcontroller and microprocessor proved highly effective. Students actively participated in group presentations, fostering a comprehensive understanding of the internal architecture, advantages, limitations, features, and applications of these technologies. Through this experiential learning process, students developed essential skills such as leadership, teamwork, critical thinking, and innovation. This case study method not only facilitated knowledge acquisition but also empowered students to apply their learning in real-world scenarios.

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Innovative Teaching Method

Title of Innovation method/activity: **Experiential Learning**

Faculty / Inventor: Dr.Veerendra Dakulagi

Course Name: Principles of Communication Systems (V semester)

Topic covered through activity:

Introduction, Why Digitize Analog Sources? Implementation of Sampling technique.

Experiential Learning through Simulation.

Simulation-based learning is a form of experiential learning that provides learners with a real-world- like opportunity to develop and practice their knowledge and skills but in a simulated environment.

Simulation-based experiential learning allows learners to absorb knowledge and practice skills in a realistic but simulated, safe environment. It is software designed to provide a realistic imitation of the controls and operation of a complex system. Simulators play highly important role in the curriculum of electronics & communication engineering. Simulators give in depth working knowledge as well as system design level knowledge to the students. Simulation-based learning is not meant to replace traditional teaching methods. It is rather complementary to the conventional teaching methods as an effective way of learning through experience by immersing learners in replicated "real-life scenarios" with guided reflections.

In this approach, a MATLAB code for the verification of sampling theorem is carried out in the classroom. The concept of sampling process for low pass signals using Nyquist criteria was very clear when the simulation(recreation method) is carried out in the class room as compared to the classical chalk and talk method.

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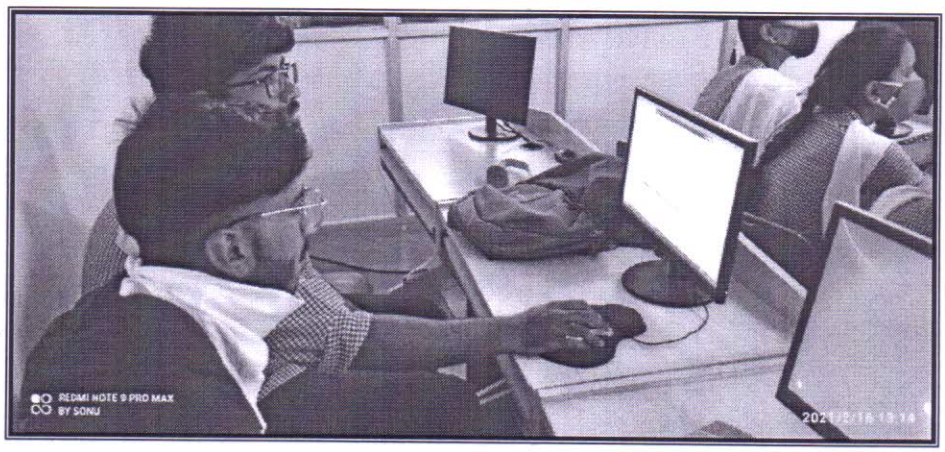

Figure2: Snapshots of Recreation Method.

The Key learning outcomes:

- Simulation based learning increase knowledge acquisition and skill performance.
- Simulation based learning knowledge increase, self-confidence, satisfaction, and collaboration.
- This activity serves as a technique to understand and to have a more clear picture on the sampling process.
- In this class, students were able to know what is engineering Nyquist criteria for the successful sampling process.

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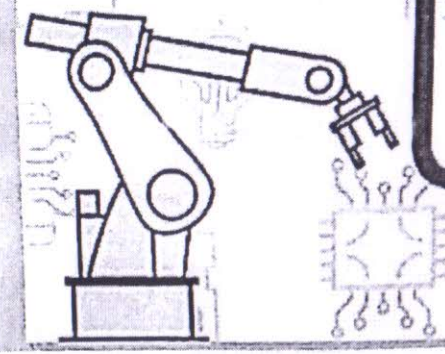
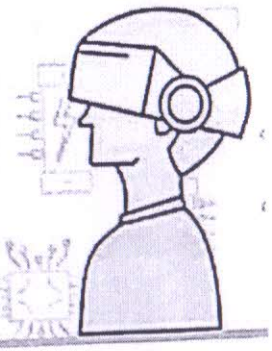
Department of Electronics and Communication Engineering

INDUSTRIAL VISIT TO

NRSC,HYDERABAD

Date - 29-03-2022





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Gmail

Dr Praveen Reddy <reddysirlogin@gmail.com>

Fwd: Permission for Industrial Visit for BE ECE students.

1 message

Guru Nanak Dev Engg College <gndtnp.resume@gmail.com> Mon, Mar 28, 2022 at 4:06 PM
To: Dr Praveen Reddy <reddysirlogin@gmail.com>

---------- Forwarded message ---------
From: <outreach@nrsc.gov.in>
Date: Thu, Mar 10, 2022 at 5:00 PM
Subject: Re: Permission for Industrial Visit for BE ECE students.
To: <placement@gndecb.ac.in>

Thanks for Interest in Space Programmes. You can visit us on Mar. 29 (about 10-30 AM to 1-00 PM)

कार्यालय, जनसम्पर्क Office of Outreach,
(जनसम्पर्क सुविधा Outreach Facility)

प्रशिक्षण, शिक्षण एवं जनसम्पर्क समूह Training, Educat on & Outreach Group
प्रबंधन प्रणाली क्षेत्र Management Systems Area (MSA)

राष्ट्रीय सुदूर संवेदन केंद्र, इसरो, National Remote Sensing Centre, ISRO,

जेएसआर कॉम्प्लेक्स / ऐश्वर्या ग्रांड के बगल में, Beside JSR Complex/Aishwarya Grand,
शाहपुर, जीदमेटला, हैदराबाद - ५०० ०५५ Shahpur, Jeedimetla, Hyderabad - 500 055

दूरभाष / Phone : 040-23884810, 23884816

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----- Original Message -----
From: placement@gndecb.ac.in
To: "outreach" <outreach@nrsc.gov.in>
Sent: Thursday, March 10, 2022 11:53:27 AM
Subject: Permission for Industrial Visit for BE ECE students.

Guru Nanak Dev Engineering College,Bidar.KA

Industrial Visit to NRSC Hyderabad

Sl.No	NAME	GENDER	AGE	
1	Md Azhar	Male	22	
2	Ameeth Parshetty	Male	22	
3	Sneha Eklure	Female	21	
4	ARCHANA	Female	21	
5	Vishal	Male	21	
6	Humera fatima	Female	22	
7	Bhavya	Female	22	
8	Rohit kote AB	Male	21	
9	priyanka	Female	22	
10	Basawasagar	Male	22	
11	Sudeep Shenikar	Male	22	
12	Mahima Starlin	Female	21	
13	Shruti	Female	21	
14	Pahuljeet kaur	Female	20	
15	Gowri.P	Female	23	
16	samreen sultana	Female	21	
17	Shreelata	Female	21	
18	Adithya Sheelvant	Male	21	
19	Jyotika	Female	22	
20	Veeresh	Male	21	
21	Kavya	Female	21	
22	Akshata	Female	21	
23	Rekha	Female	21	
24	Rahul Shambhu	Male	21	
25	Vaishnavi	Female	22	
26	Asha	Female	22	
27	Nagaraj biradar	Male	21	
28	AMMARA FIRDOUS	Female	20	
29	Divya	Female	21	
30	Mahalaxmi shetkar	Female	22	
31	Aishwarya Chidguppika	Female	22	
32	Kosgi Vinay Kumar	Male	22	
33	Suneeta	Female	20	
34	Usha rani	Female	22	
35	rohit j	male	21	
36	pratiskha	female	21	
37	saba yasmeen	female	21	
38	shagufta	female	21	
39	shweta wadekar	female	21	
40	siddramappa	male	22	
41	shweta bhalke	female	21	
42	pooja	female	21	
43	sushmita	female	21	
44	ummeaiman	female	21	
45	Sudharani	Female	21	
46	Supreet Akash	Male	21	
47	Praveen Reddy	Male	35	
48	Manpreet Sigh	Male	35	
49	Shravan	Male	35	
50	Pavan M	Male	33	
51	Md.Bakhar	Male	52	
52	Anuradha	Female	40	
53	Savita	Female	50	
54	Namratha	Female	44	

Head of E & CE Dept.
G.N.D Engineering College
BIDAR-585 403. (K S)

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Feedback Form For Industrial Visit to NRSC

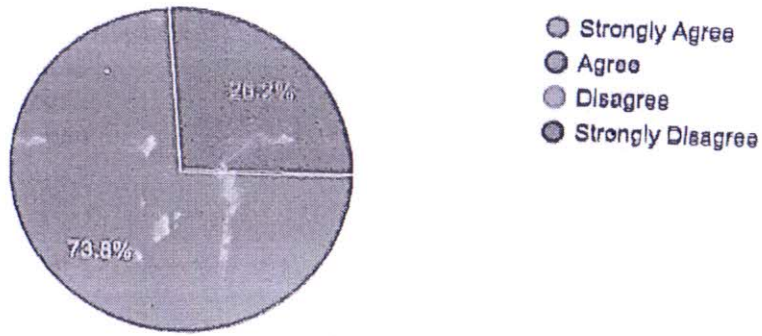
42 responses

Publish analytics

Whether Industrial Visit to NRSC was Informative?

Copy

42 responses



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NRSC Visit time was sufficient?

42 responses

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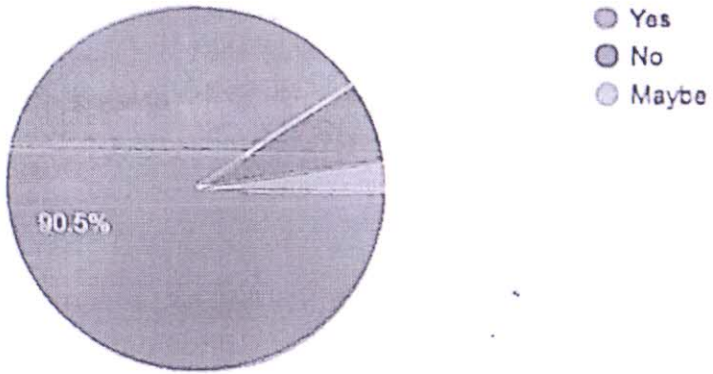
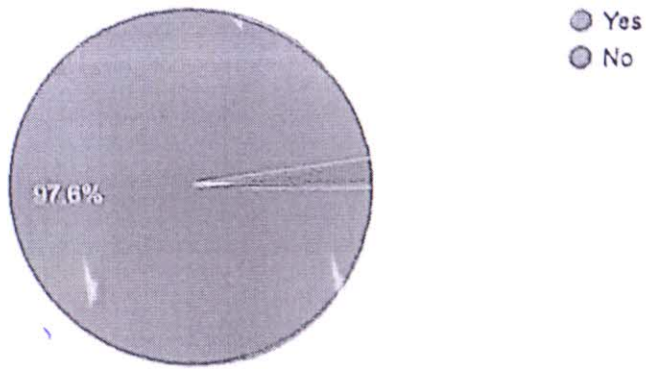


Do you want to Visit any other Industry?

42 responses

Copy



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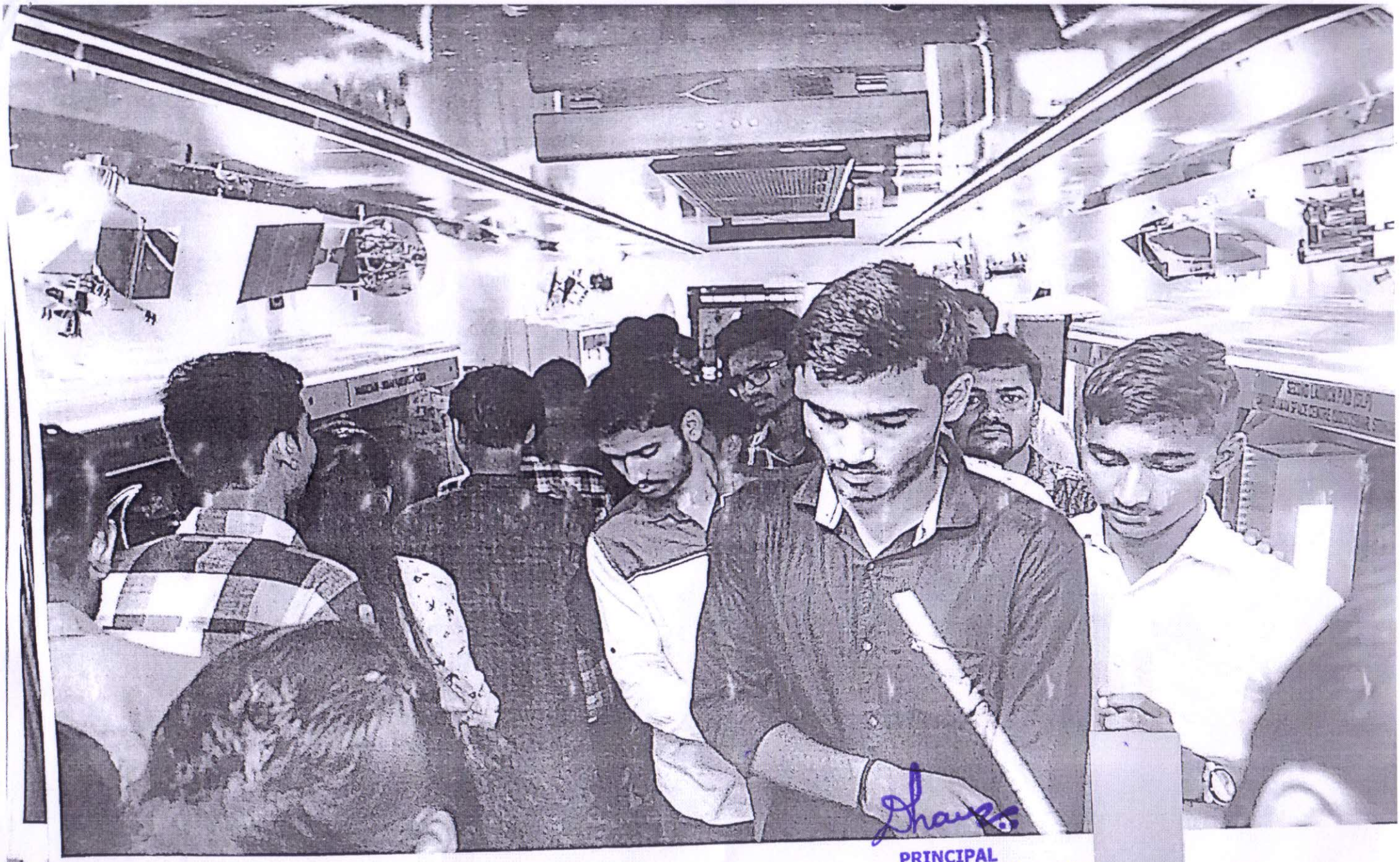

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CERTIFICATE
OF INTERNSHIP

Join - Interact - Learn - Practice - Be Industry Ready

THIS IS TO CERTIFIY THAT

Mr./Ms. MEGHANA

of GURU NANAK DEV ENGINEERING COLLEGE, BIDAR

has successfully completed 30 days 80 Hours of Internship on Internet of Things and also worked on Live industry project and presented a detailed report under the aegis of Expertshub.

During the Internship tenure he/she has shown very good technical skills and his/her conduct throughout the internship was good.

Project Title : GPS Based Home Appliance Control

Internship Tenure : Oct 01st to Oct 30th, 2020

S.No : EH/Intern/9261862174438

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Kirubakaran Reddy
Founder & Director
Expertshub

An ISO 9001:2015 Quality Certified Organization

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LIST OF INNOVATIVE PROJECTS
ACADEMIC YEAR (2019-20)

SL.NO	PROJECT TITLE
1	Blue buzz
2	Robust infrastructure for preventing food wastage using Web, iot and gps.
3	Electric Motor-Powered Multigrain Cuttėr
4	Lamani Language synthesizer
5	Robust crop yeild enhancing technology
6	Six legged kinematic Walker
7	Smart blind stick
8	V-tooth device for hearing impaired/aged people
9	Robust sewage water treatment system for homes
10	Solar powered smart water bottle

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GURUNANAK DEV ENGG COLLEGE ,BIDAR
DEPT OF E&CE
6TH A MINI PROJECT LIST WITH GUIDE NAME AND TITLE ACAD. YEAR:- 2021-22

GROUP NO	STUDENT NAME	USN	MINI PROJECT GUIDE	TITLE
GROUP1	Hadole Shubham	3GN19EC012	Dr.Premala.P	Anti sleep alarm for drivers
	Nitin	3GN19EC031		
	Nitish Mulge	3GN19EC032		
GROUP2	M A Muneeb	3GN19EC017	Dr.Veerendra.D	ORBI-V : An Open Source and modulato RISC-V Core
	Md Motesim Ahmed	3GN19EC022		
GROUP3	Sonali	3GN19EC050	Dr.Veerendra.D	Performance Analysis of adaptive beam former for mobile communication
	Nishu	3GN19EC030		
	Anjali	3GN19EC005		
GROUP4	Maheshwari	3GN19EC019	Dr.Savita S	Automatic plant watering systen using Arduino
	Nageshwari	3GN19EC023		
GROUP 5	Atif shazeb	3GN19EC007	Prof.Rajendra.K	Face recognition using Python(Open CV)
	Kamran Abdul Rafey	3GN19EC014		
	Naser	3GN19EC024		
GROUP6	Bhagyashri	3GN20EC400	Prof.Namratha.E	Mobile phone detector
	Basawshri	3GN19EC009		
	Anjali	3GN19EC004		
GROUP7	Pallavi	3GN20EC401	Prof.Nitin.K	Laser security alarm system
	Nisha	3GN18EC023		
	Anjali	3GN19EC005		
GROUP8	Nausheen hassan	3GN19EC025	Prof.Shilpa.B	Wireless Notice Board
	Nazneen fatima	3GN19EC027		
	Nisha joshi	3GN19EC029		

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GROUP 9	Nikhilesh	3GN19EC028	Prof.Santosh .Y	Clap Switch
	V.Anil	3GN18EC060		
GROUP 10	Aishwarya R	3GN19EC002	Prof.Soni Mankari	Home Automation using Node MCU and Web Server
	Kavyanjali	3GN19EC015		
	Madhu Patil	3GN19EC018		
GROUP 11	Ankur Kuttabadkar	3GN19EC006	Prof.Soni Mankari	Automatic Toll tax system using Arduino
	Augustine	3GN19EC008		
	Jagadish	3GN19EC013		

Coordinator

HOD(ECE)

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GURU NANAK DEV ENGINEERING COLLEGE BIDAR
ELECTONICS & COMMUNICATION ENGINEERING DEPARTMENT

Academic Year: 2021-2022 **Semester: VII (A/B)**

APPROVED LIST OF PROJECT WORK

Sl. No.	Group No.	Univ. Seat No. of Student	Name of Student	Broad area of the Project	Internal Guide	Title of the Project	Remarks
7.	3	3GN18EC032	Rabiya Basreen	**Embedded System**	**Prof. Pradeep K**	Dam Water Level Monitoring And Alerting System Using IOT	
8.		3GN18EC035	Rohini				
9.		3GN18EC057	Usha Rani				
10.	4	3GN18EC038	Saba Yasmeen	**Embedded System**	**Dr. Veerendra D**	An Emotion Based Music Player For Android	
11.		3GN18EC041	Shagufta Naaz				
12.		3GN18EC046	Shweta Bhalke				

Date: 30.10.2021

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Signature: .....

Head of Department

GURU NANAK DEV ENGINEERING COLLEGE BIDAR

Department of Electronics and Communication Engineering

NOTE 18-04-2023

Subject: To sanction amount of Rs 7,000/ - (Seven thousand only) to attend seminar at College of Engineering, Pandharpur on 21st April 2023.

Concerning the above cited subject, Our department final year students(six students with one faculty) are presenting seminar in College of Engineering, Pandharpur on 21st April 2023.

In this regard, I kindly request you to sanction the TA/DA amount.

Thanking You

HOD(ECE) PRINCIPAL VICE CHAIRPERSON CHAIRMAN

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Guru Nanak Dev Engg. College, Bidar

Dattatray Surykant Sawant
Chair, Student Activities Committee (SAC) and EXECOM Member
IEEE Bombay Section, Mumbai, INDIA
Senior Member IEEE

LOCAL ADVISORY COMMITTEE

1. Dr. B. P. Ronge
   Principal, COE Pandharpur
2. Prof. M. M. Pawar
   Campus Incharge
3. Dr. P. M. Pawar
   Dean Academics & HOD, Civil Dept.
4. Dr. R. R. Gidde
   Dean Administration, COE Pandharpur
5. Dr. M. S. Mathpati
   Dean Student COE Pandharpur
6. Dr. D. S. Choudhari
   Dean Publicity Protocol, COE Pandharpur
7. Mr. A. A. Mote
   Dean TPII, COE Pandharpur
8. Mr. S. M. Khomane
   Dean TPII, COE Pandharpur
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10. Dr. Mrs. M. M. Pawar
    HOD E&TC, COE Pandharpur
11. Dr. Mrs. D. A. Tamboli
    HOD EE, COE Pandharpur
12. Dr. S. S. Wangikar
    HOD Mech, COE Pandharpur
13. Dr. Mrs. S. P. Pawar
    HOD CSE, COE Pandharpur
14. Dr. S. A. Lendave
    HOD F.Y.B.Tech., COE Pandharpur

Last Date of Registration: 20th April 2023

Registration Fees:

1) For Project Competition	:	Rs. 150/- per group
2) For Paper Presentation	:	Rs. 100/- per group
3) For Poster Presentation	:	Rs. 100/- per group
4) For Win to Buzz	:	Rs. 100/- per group
5) For Programming mania	:	Rs. 100/- per group

Registration Link:
https://forms.gle/h4CnAuHFn46SUwaC6
For any requirement related to project please send email: jshallur@coe.sveri.ac.in

Prizes for Each Department:

Events	1st Prize	2nd Prize	3rd Prize
Project Competition Under AICTE-SPICES	5000/-	3000/-	2000/-
Technical Paper Presentation	2000/-	1500/-	1000/-
Technical Poster Presentation	2000/-	1500/-	1000/-
Win to Buzz	2000/-	1500/-	1000/-
Programming Mania	2000/-	1500/-	1000/-

Note : Certificate to Each Participants

CHIEF PATRON

Dr. B. P. Ronge
Founder secretary & Principal,
SVERI's COE Pandharpur

ORGANIZING SECRETARY

Dr. Mrs. M. M. Pawar
Professor & HOD ENTC, SVERI's COE Pandharpur.
Dr. Mrs. D. A. Tamboli
Associate Professor & HOD EE, SVERI's COE Pandharpur
Mr. J. S. Hallur
Assistant Professor & Overall Coordinator, SVERI's COE Pandharpur

COORDINATOR

Mr. M. A. Deshmukh (9970277150)
Miss. S. D. Pujari (8600753875)
Mr. A. A. Garad (9284706027)

LOCAL ORGANIZING COMMITTEE

1. Mrs. J. S. Shinde
2. Ms. N. P. Kulkarni
3. Mr. S. A. Inamdar
4. Mr. A.B. Chounde
5. Mr. J. L. Musale
6. Mr. P. S. Deshmukh
7. Mr. S. A. Atole
8. Ms. N. T. Pujari
9. Ms. S. S. Pawar
10. Dr. M. K. Mishra
11. Ms. S. S. Jadhav
12. Mrs. S. Y. Abhangrao
13. Ms. M. S. Biswas
14. Ms. S. S. Gawade
15. Ms. Megha Sontakke
16. Ms. V. V. Gore
17. Ms. S. B. Jagtap

CONTACT EMAIL-ID

jshallur@coe.sveri.ac.in /rbpawar@coe.sveri.ac.in

PRINCIPAL

E-LOGIC - 2K23

On
21th April 2023

Organized by
Department of Electronics & Telecommunication Engineering
(Under ELITE Forum)
&
Department of Electrical Engineering

SVERI
Engineering for Excellence

SVERI's
College of Engineering, Pandharpur

Gopalpur-Ranjani Road Gopalpur, Pandharpur
Pin: 413304 Dist-Solapur.
Website: www.sveri.ac.in www.eelogic.sveri.ac.in

In Collaboration With
SVERI's IEEE Student Chapter STB10002
IEEE, Bombay - Section

N.B.A.

A+ NAAC

IEEE BOMBAY SECTION

About the College:

Pandharpur is well known holy place where Lord Shri Vithal temple is located on bank of the river Chandrabhaga. It is recognized as spiritual capital of Maharashtra and popularly known as "South Kashi" of India. Shri Vithal Education and Research Institute (SVERI), a charitable trust formed by devoted technocrats, established its first Project, the SVERI's College of Engineering, Pandharpur in 1998, with approval from AICTE, New Delhi. It has been affiliated to P.A.H. Solapur University, Solapur. The Engineering College is ISO 9001:2015 certified institute and accredited by **NBA Accredited all Eligible UG Programs, NAAC A+, TCS.**

Salient features of Electronics & Telecommunication Engineering Department

The department was established in the year of 1998. **In year 2020-21, 173+ offers have received and in year 2021-22 178+ offers have received. More than 80 students of ENTC department have been placed in reputed organizations such as TCS, L&T Infotech, Wipro, Capegemini, Bharat Forge, Zensar, HCL, Birlasoft, VOIS, Tech Mahindra Qspider and many more till date.** The department has 32 Qualified and Dedicated Faculty Members with the specialization in Different areas including Doctorates faculty. Department of E&TC have **UG (120 Intake), PG (18)** and **PhD** programs. The faculty has published **350+** technical papers in international journals and conferences, also **30** technical books have been written in International Publications. Further, the department has completed various research projects of **44 lakhs rupees.** The ENTC department has tradition to organize a national level STTP and a national level conference every year. Department has signed MoUs with different industries through which students benefitted by attending training based on ARDUINO and other micro controller as well as sponsorship for final year project.

Industrial Visits are organized every semester for every class and expenditure assisted by college. The department is having well equipped labs with investment over Rupees **2,28,58,037/-** The ENTC department has excellent academic record in Solapur University (25 University Rankers). The institute also provides financial support for student to appear GATE exam, industrial visit, to attend workshops, to attend conferences.

Excellent Placement of E&TC Department

1. WIPRO	wipro	:33
2. TCS	TCS	:14
3. Infosys	Infosys	:13
4. Capgemini	Capgemini	:11
5. Other Companies		:70+

Salient features of Electrical Engineering Department

The department was established in the Academic Year **2018-2019** with intake of **60.** The department has **12** qualified and dedicated faculty members including one doctorate faculty and two are pursuing their doctoral degree. These faculty members have specializations in various areas. The departmental faculty members have published papers in various conferences, and journals. The department has signed MoUs with different industries through which students are benefited with training, industrial visits and interactions with industry experts. In each semester, Industrial Visits are organised for each class and financial support is given by our institute. The department has 8 well-equipped laboratories & ICT enable of classroom. The institute also provides financial support to the students to appear for GATE and NPTEL examinations, to attend the workshops, conferences and, trainings etc. The Electrical department has excellent academic record in P.A.H. Solapur University, Solapur (17 University Rankers)

Rules for Project Competition

- Computers with required software, Power supplies, will be made available for demonstration of projects.
- Participated Students should come along with Valid **Identity Proof.**
- More than 3 members in a group are allowed.

PRINCIPAL
Guru Nanak Dev Engg. College, Bidar

Contact Person:

Mr. J. S. Hallur	:	9975090344
Mr. S. B. Waghchavare	:	9665187875
Mr. R. B. Pawar	:	9665227585

How to Reach SVERI:

Direct Buses to Pandharpur are available. From Pandharpur bus stand, Pick up and Drop facility will be made available for participants as per requirement.

Contact Person:

Mr. S. A. Inamdar	:	9922818946
Mr. A. B. Chounde	:	9922341043

Registration Form

Name: _____

Branch: _____

Institute Address: _____

_____

Accommodation (Y/N): _____

Contact Details:

Mob. No.: _____

Email Id: _____

Amount in Rs: _____

Date: _____

Signature of Participant: _____

Registration Link:

https://forms.gle/h4CnAuHFn46SUwaC6

About E²LOGIC 2K23

Project Competition & Exhibition

The goal of this project competition & exhibition is to provide platform for all engineering students to showcase their talents. This will help to bridge the gap between industry and academics, which enhances creative skills and research attitude.

Shri Vithal Education & Research Institute's

College of Engineering, Pandharpur

SVERI Engineering for Excellence

• All eligible UG Programmes NBA Accredited • NAAC A+ Accredited • Approved by A.I.C.T.E., New Delhi • Affiliated to P.A.H. Solapur University, Solapur
• Accredited by The Institution of Engineers (India) & Tata Consultancy Services (TCS) • ISO 9001 : 2015 Certified Institute

SVERI 25 रौप्य महोत्सवी वर्ष 2022-23

ASHRAE Shri Vithal Education & Research Institute Pandharpur Branch

A+ NAAC (3.46/4.00 CGPA)

N.B.A.

National Level Event

E²LOGIC 2K23

ELITE

IEEE BOMBAY SECTION

Certificate

This is to certify that Mr./Ms. VEERENDRA PATIL

from GNDEC B. Bidar participated

Successfully / Secured 3rd position in "E²LOGIC 2K23" paper presentation

Organized by Department of Electronics &Telecommunication Engineering and Department of Electrical Engineering Sponsored by SVERI-SPICES and in collaboration with SVERI's IEEE student chapter, IEI Kolkata, IEI Solapur student chapter, IEEE Bombay Section and ASHRAE SVERI Branch, on 21/04/2023.

IEI

IEEE Branch Counselor

E²LOGIC Co-ordinator

ELITE Co-ordinator

HOD E&TC

PRINCIPAL

GURU NANAK DEV ENGINEERING COLLEGE, BIDAR

Department of ECE Engineering

List of Students participated in various competitions

Academic year: 2019-2020

S. No	Participants	Event	Organized Institute
1	Aishwarya	Poster presentation	Sanjay Ghodawat University, Kolhapur,Maharashtra
2	Ameeth Parshetty	Technical paperpresentation	Sanjay Ghodawat University, Kolhapur,Maharashtra
3	Ajay , Vaibhavi & Shambhavi	Hackathon "SmartTantrota-19"	PDACE, Kalaburagi
4	Aadesh	Zeal COVID-19 Hackathon	Zeal Institutes, Pune,Maharashtra
5	Jashvini	Zeal COVID-19 Hackathon	Zeal Institutes, Pune, Maharashtra
6	Gayatri	Zeal COVID-19 Hackathon	Zeal Institutes,Pune, Maharashtra
7	Pooja	Poster presentation	Sanjay Ghodawat University, Kolhapur, Maharashtra
8	Gadgi Vishal	Code Rush	Sanjay Ghodawat University, Kolhapur, Maharashtra
9	Aditya	Technical paper presentation	Sanjay Ghodawat University,Kolhapur, Maharashtra
10	Pahuljeet	Debate, VTU Youth Festival	SDM, Dharwad
11	Sneha	Campus Drive	SVERI College of Engineering, Pandharpur, Maharashtra
12	Shrushti	Campus Drive	SVERI College of Engineering, Pandharpur, Maharashtra
13	Mayuri kulkarni	Campus Drive	SVERI College of Engineering, Pandharpur
14	Laxmi	Campus Drive	SVERI College of Engineering, Pandharpur, Maharashtra
15	Laxmi	Posterize	SVERI College of Engineering, Pandharpur, Maharashtra

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16	Syed Sarfaraz	Hackathon "Smart Tantrota-19"	PDACE, Kalaburagi
17	Safiuallah Alvi	Hackathon "Smart Tantrota-19"	PDACE , Kalaburagi
18	Suhas Anand	Hackathon "Smart Tantrota-19"	PDACE , Kalaburagi
19	Meghana	Hackathon "Smart Tantrota-19"	PDACE, Kalaburagi
20	Archana	Group Singing, VTU Youth Festival	SDM, Dharwad
21	Aishwarya	Group Singing VTU Youth Festival	SDM, Dharwad
22	Akshata	Group Singing VTU Youth Festival	SDM, Dharwad
23	Kavya	Group Singing VTU Youth Festival	SDM, Dharwad
24	Jyothika	Solo Dance, VTU Youth Festival	SDM, Dharwad
25	Asha	Group Singing, VTU Youth Festival	SDM, Dharwad
26	Amandeep Singh	Group Dance, VTU Youth Festival	SDM, Dharwad
27	Shivani	Fun Zone	SVERI College of Engineering, Pandharpur, Maharashtra
28	Prasad	Fun Zone	SVERI College of Engineering, Pandharpur, Maharashtra
29	Pratap singh	Fun Zone	SVERI College of Engineering, Pandharpur, Maharashtra
30	Sushmita	Filmy Spartans	GNDEC,Bidar
31	Ameeth	Carrom	GNDEC,Bidar
32	Sanjeevini	Rangoli	GNDEC,Bidar
33	Vishal	Carrom	GNDEC,Bidar
34	Jyotika	Star-o-Buzz(dance)	GNDEC,Bidar
35	Sahil	Star-o-Buzz(dance)	GNDEC,Bidar

HOD
(E&CE)

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GURU NANAK DEV ENGINEERING COLLEGE, BIDAR

Department of ECE Engineering

List of papers published by students

Academic Year: 2019 -2020

S.No.	Participants	Title of the paper	Journal Name
1	Ajay Dhage	Overdose Detection using sensor technology	GRIN
2	Vaibhavi	Overdose Detection using sensor technology	GRIN
3	Shambhavi	Overdose Detection using sensor technology	GRIN
4	Ajay Dhage	Intel Edison Kit and IOT based Smart help monitoring system	IJSRD
5	Arshiya	Analysis of MAC protocols for Wireless Sensor Network	IJIRT
6	Syeda Nimra	Analysis of MAC protocols for Wireless Sensor Network	IJIRT
7	Ratnamala	Analysis of MAC protocols for Wireless Sensor Network	IJIRT
8	Madhushree	Solar powered auto irrigation system	IRE
9	Heena	Solar powered auto irrigation system	IRE
10	Madhavi	Solar powered auto irrigation system	IRE
11	Anup	Smart and Economic Farming Using IoT	IRE
12	Pragati	Smart and Economic Farming Using IoT	IRE
13	Priyanka	Smart and Economic Farming Using IoT	IRE
14	Kavya	Measure of Diameter an object within an image using Matlab	Presented in NCAECC 2020,GNDEC BIDAR
15	Manuja	Measure of Diameter an object within an image using Matlab	Presented in NCAECC 2020,GNDEC BIDAR
16	Shweta	Measure of Diameter an object within an image using Matlab	Presented in NCAECC 2020,GNDEC BIDAR

HOD
(E&CE)

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GURUNANAK DEV ENGINEERINGCOLLEGE
BIDAR

Department of Electronics and Communication Engineering Department

ACADEMIC YEAR: 2021-22

Date: 21-09-2021

Circular

All the students of Electronics and Communication department are hereby informed to actively participate in **"TREE PLANATATION"** on 23-09-2021 in the Guru Nanak Dev Engineering college campus without fail. Attendance will be recorded.

Dept.NSS Co-ordinator

HOD

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GURUNANAK DEV ENGINEERINGCOLLEGE
BIDAR

Department of Electronics and Communication Engineering Department

A

Report on

"TREE PLANATATION"

Organized by

"ECE Department under NSS"

PRINCIPAL
Guru Nanak Dev Engg. College, Bidar

GURUNANAK DEV ENGINEERINGCOLLEGE
BIDAR

Department of Electronics and Communication Engineering Department

Title- "TREE PLANTATION"

Date: 23-09-2021

Time of event: 10:00 AM

Event Type: Offline

TREE PLANTATION

The students of Electronics and Communication Engineering department conducted the programme of sapling plantation. This drive was conducted under the programme officer Dr. Kishan Singh along with Prof. Prof.Soni Mankari, Prof.Pradeep Karanje as coordinators.

Tree planting is recognized as one of the most engaging, environmentally-friendly activities that people can participate in to help the planet when done properly. Trees provide numerous long-term and short-term benefits. They not only look nice, but they also remove and store carbon from the atmosphere, slow heavy rain and thus reduce the risk of flooding, improve air quality, and reduce the urban heat island effect by reflecting sunlight and providing shade.

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Department of Electronics and Communication Engineering Department

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Benefits of Tree Plantation:

They are so valuable and significant that their applications have only grown to meet the demands of our modern lifestyles. Initially, the tree's wood was used as fuel, and the fruits were devoured by people. The shade was utilized to keep cool in the summer and the fire was used to keep warm in the winter.

Program Outcome:

We recognize that one of the most serious issues in the current state is the devaluation of trees. To make our world greener and better, we must pay more attention to tree planting in the future. Individual contributions should be made first. We should also put a stop to tree cutting and remember that without trees, there will be no life. The tree is very important in our lives. Everyone should be aware of the importance of tree planting and should motivate and encourage others to do the same.

Dept.NSS Co-ordinator

HOD

GURUNANAK DEV ENGINEERINGCOLLEGE
BIDAR

Department of Electronics and Communication Engineering Department

Student list

Sl.No	USN	Name
1	3GN21EC001	ABDUL AWAIS
2	3GN21EC002	ABHISHEK
3	3GN21EC003	ABHISHEK
4	3GN21EC004	ABHISHEK BIRADAR
5	3GN21EC005	ADIBA MAHWISH
6	3GN21EC006	ADITYA
7	3GN21EC007	AISHWARYA
8	3GN21EC008	AISHWARYA
9	3GN21EC009	AJAY PATIL
10	3GN21EC010	AKHILESH
11	3GN21EC011	AKSHATA
12	3GN21EC012	ALWIN MARK
13	3GN21EC013	AMULYA
14	3GN21EC014	ASHITOSH
15	3GN21EC015	ASHUTOSH
16	3GN21EC016	AZZA ASSAD
17	3GN21EC017	BASAVAKUMAR
18	3GN21EC018	BHAGYASHREE
19	3GN21EC019	BHAVANI
20	3GN21EC020	BHIMASHANKER NIJAMPURE
21	3GN21EC021	DEEPTI
22	3GN21EC022	DEVIKA GANGSHETTY
23	3GN21EC023	DHANALAXMI
24	3GN21EC024	DIANA
25	3GN21EC025	EMRALD DAVID
26	3GN21EC026	FARHAT FATIMA
27	3GN21EC027	G AKSHATA

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BIDAR

Department of Electronics and Communication Engineering Department

28	3GN21EC028	G DEEKSHA
29	3GN21EC029	GANGA PATIL
30	3GN21EC030	HARSHPREET SINGH JUNEJA
31	3GN21EC031	JAGADISH
32	3GN21EC032	JASPREET SINGH
33	3GN21EC033	KARAN
34	3GN21EC034	KHADEER AHMED
35	3GN21EC035	KRISHNA
36	3GN21EC036	MANGALA
37	3GN21EC037	MANJULA
38	3GN21EC038	MANJUNATH
39	3GN21EC039	MANSI
40	3GN21EC040	MD KAMRAN
41	3GN21EC041	MD SOHEL
42	3GN21EC042	MD RIZWAN AHMED
43	3GN21EC043	MOHD AFTAB AHMED
44	3GN21EC044	MONIKA
45	3GN21EC045	NAMRATA
46	3GN21EC046	P MAHESH CHARY
47	3GN21EC047	PITRE MAYUR BASWARAJ
48	3GN21EC048	POOJAREDDY
49	3GN21EC049	PRAMOD
51	3GN21EC051	PRATEEKSHA
52	3GN21EC052	PRATHVIRAJ
53	3GN21EC053	PRAVEEN
54	3GN21EC054	PRAVEEN
55	3GN21EC078	SRIDEVI

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GURU NANAK DEV ENGINEERING COLLEGE, BIDAR.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Virtual Lab

Course Title: Network Theory **Course Code:** 18EC32

Academic Year: 2020-2021 **SEM:** III A&B

Name of the Faculty: Prof. Shilpa B

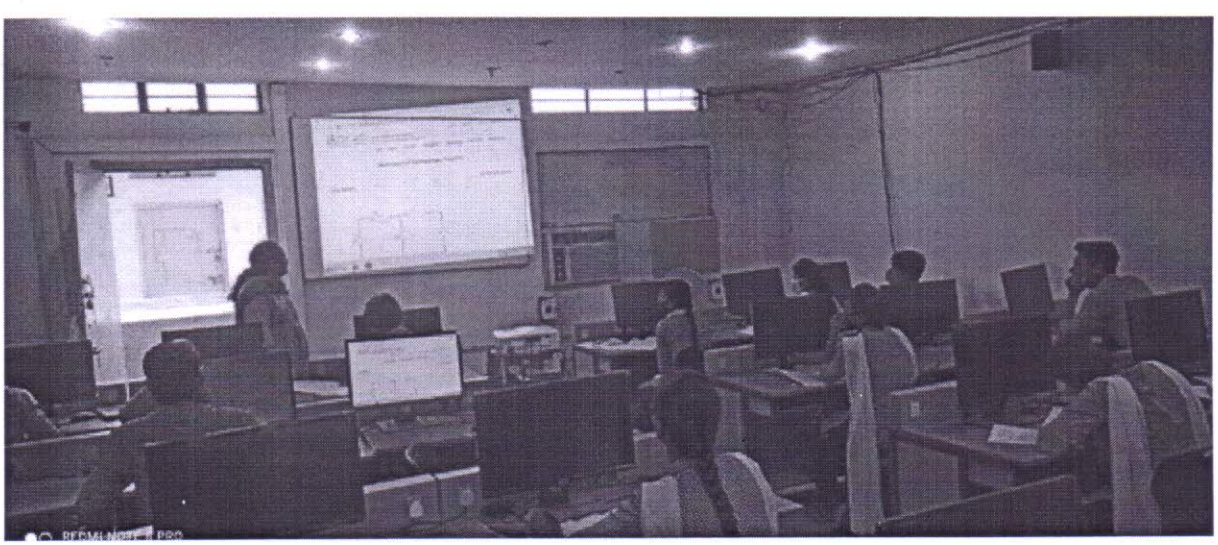
Title: Network Theorems using Virtual Lab(VLAB)

- **Objectives:**

To understand and verify network theorems using VLAB.

Description:

Virtual laboratory provides a chance to perform experiments for theoretical subjects using the internet and visual aids without having the equipments at their end. Demonstrated Verification of network theorems such as Superposition ,Thevenin's ,Norton's etc using VLAB .



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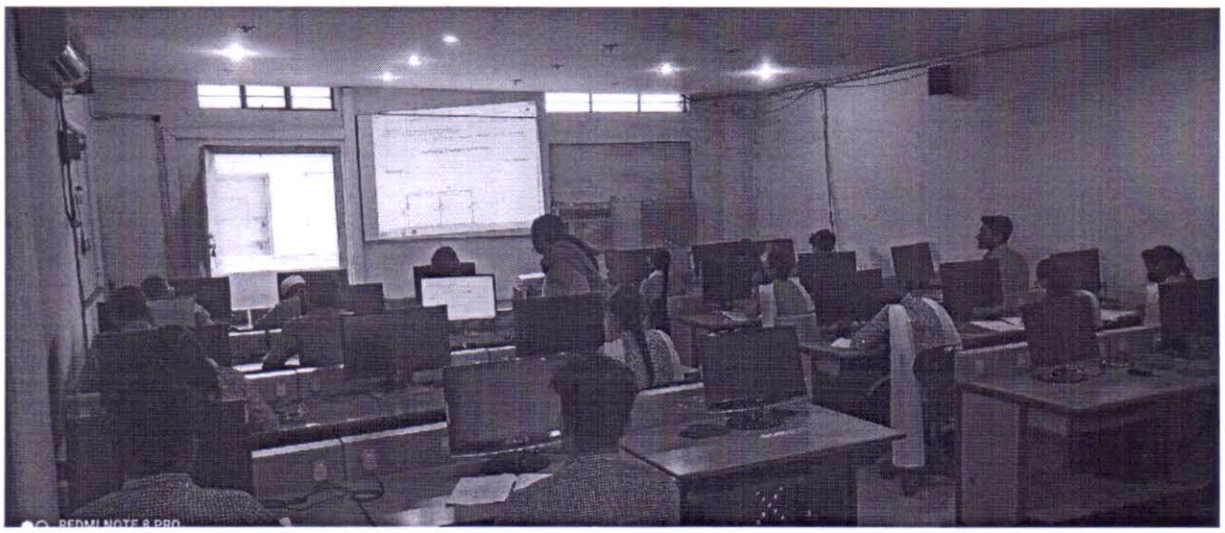

Outcomes:

1.Students learned to verify theorems using practically using VLAB.

2.Students got more knowledge about network theorems.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Notice

All the students of III semester are hereby informed to attend the Practical demonstration of **"Network Theorems using Virtual Lab(VLAB)"** on Date: 30.1.21

HOD

Course In charge

Prof.Shilpa Biradar

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

NOTICE

Date: 29.12.2021

All the staff of ECE department are hereby informed to attend meeting on 30/12/2021 in Department library at 4.00p.m.

Agenda of the meeting: Course Selection for Swayam Nptel Courses for the upcoming semester.

HOD(ECE)

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 30.12.2021

Minutes of Meeting

A meeting of HOD with staff members was held on 30/12/21 at 4.00 pm to discuss about Course selection for Nptel .

Agenda of the meeting: Course Selection for Swayam Nptel Courses for the upcoming semester(2021-22 EVEN).

Following Staff members were present:

1. Dr. Md.Bhakar
2. Dr.Kishan Singh
3. Dr.Anuradha.S
4. Prof.Ramesh Patil
5. Dr.Veerendra.D
6. Dr.Savita.Soma
7. Prof.Praveen Reddy
8. Dr.Premala.P
9. Prof.Rajendra.K
10. Prof.Namratha
11. Prof.Nitin
12. Prof.Shilpa
13. Prof.Ramya.S.Kudre
14. Prof . Santosh.Y
15. Prof. Pavan M
16. Prof.Pradeep
17. Prof.Soni.M
18. Prof.Huggi Pooja
19. Prof.Shweta.G

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Dr.Md.Bhakar, The HOD, (E&CE) Dept welcomed all the staff of the department and briefed about the agenda. With reference to circular dated 29.12.2021 .

The following points were discussed regarding selection of courses for nptel e-learning:

3. Dr.Kishan Singh suggested that one of the prime importance of e-learning is that it helps students and teachers develop advanced skills. E-Learning provides scalability which helps in providing training. All students can receive the same type of syllabus, study materials and train through E-Learning. Through E-Learning, you can save time, money and reduced transportation cost. so, E-Learning is cost-effective compared to traditional learning.

4. Dr.Premala Patil shared that wealth of research shows that e-learning can be as effective or even more effective than face-to-face instruction when appropriate instructional methods are used. It can, therefore, be effective for instructional impact. Also listing the advantages of it such as Variety of Courses. From nursing to neuroscience to certification as a life coach, e-learning has democratized education, Value for Money. In India, it is a fact that many professors lack accountability, Faculty Feedback, Room for All, no age bar, World Classroom.

As per the decision of HOD the course selection list along with mentor are attached.

HOD(ECE)

Copy to:

1.Staff Circulation

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGG

SWAYAM NPTEL LOCAL CHAPTER

Ref:GNDECB/SWAYAM NPTEL

SEM:21-22 EVEN

SL.NO	BRANCH	SEMESTER	COURSE CODE	COURSE NAME	SECTIONS	MENTORS NAME
1	ECE	IV	18EC45	Signals and Systems	2	Prof.Pavan.M
2		VI	18EC61	Principles of digital communication	2	Dr.Md.Bakhar
3		VIII	--	Fundamentals of MIMO wireless communication	2	Prof.Soni.M

HOD(ECE)

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noc22_ee1	Principles of Digital Communications	patilsanju089@gmail.com	Sanjeevini	+91 86184 79195	2021 student	3GN19EC04	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	kulkarnisanyukta97@gmail.com	Sanyukta	+91 99020 64120	2023 student	3529/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	nasermd501@gmail.com	Naser	+91 91484 20502	2019 student	3GN19EC02	Electronics and Communication Engineering	1
noc22_ee1	Principles of Digital Communications	sukeshini3534@gmail.com	SUKESHINI.G.RED	+91 96864 27430	2023 student	3534/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	vaishnavipampad24834@gmail.com	Vaishnavi12	+91 78937 84668	2023 student	3538/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	motesim99@gmail.com	Md Motesim Ahn	+91 99865 01170	2019 student	304/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	adilhussaini841@gmail.com	Syed Adil Hussain	+91 96868 54549	2019 student	3gn19ec054	Electronics and Communication Engineering	1
noc22_ee1	Principles of Digital Communications	padmasrjoshi33@gmail.com	Padma	+91 93805 45034	2019 student	3520/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	sinusrinivas925@gmail.com	Padaminay srinivi	+91 95359 47151	2019 student	3006/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	maheshwaripatil049@gmail.com	Maheshwari	+91 76193 27184	2023 student	303/19	Electronics and Communication Engineering	3
noc22_ee1	Principles of Digital Communications	nishupatil157@gmail.com	Nishu.patil	+91 93535 69343	2023 student	3005/19	Electronics and Communication Engineering	5

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Guru Nanak Dev Engg. College

This certificate is computer generated and can be verified by scanning the QR code given below.

Roll No: NPTEL22HS29S23260024

To
M A MUNEEB
HOUSE NO. 9-5-466 HYDER COLONY
CHIDRI ROAD BIDAR
BIDAR
KARNATAKA - 585401
PH. NO :7019066354

Score	Type of Certificate
>=90	Elite+Gold
75-89	Elite+Silver
>=60	Elite
40-59	Successfully Completed
<40	No Certificate

No. of credits recommended by NPTEL:3
An additional 1 credit may be awarded if the University deems it fit, based on the actual student effort involved.

NPTEL Online Certification

(Funded by the MoE, Govt. of India)

This certificate is awarded to

M A MUNEEB

for successfully completing the course

Patent Law for Engineers and Scientists

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with a consolidated score of **57** %

Online Assignments	22.81/25	Proctored Exam	33.99/75

Total number of candidates certified in this course: **588**

Devendra Jalihal
Prof. Devendra Jalihal
Chairman
Centre for Continuing Education, IITM

Jan-Apr 2022
(12 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras

Indian Institute of Technology Madras

swayam

This certificate is computer generated and can be verified by scanning the QR code given below.

Roll No: NPTEL22CS32S33480084

To
SHILPA BIRADAR
OLD ADARSH COLONY,SAI BABA TEMPLE
ECE DEPT.,GND ENGG. COLLEGE
BIDAR
KARNATAKA - 585401
PH. NO :8095247620

Score	Type of Certificate
>=90	Elite+Gold
75-89	Elite+Silver
>=60	Elite
40-59	Successfully Completed
<40	No Certificate

No. of credits recommended by NPTEL:1
An additional 1 credit may be awarded if the University deems it fit, based on the actual student effort involved.

Elite

NPTEL Online Certification

(Funded by the MoE, Govt. of India)

This certificate is awarded to

SHILPA BIRADAR

for successfully completing the course

Python for Data Science

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with a consolidated score of 69 %

Online Assignments	20/25	Programming Exam	21.75/25	Proctored Exam	27.67/50

Total number of candidates certified in this course: 3390

Devendra Jalihal
Prof. Devendra Jalihal
Chairman
Centre for Continuing Education, IITM

Jan-Feb 2022
(4 week course)

Prof. Andrew Thangaraj
NPTEL, Coordinator
IIT Madras

Indian Institute of Technology Madras

swayam

ICT Tool
Report on

"E-tools for Palgarism & Grammar"

Organized by

"Faculty of Electronics and Communication Engineering"

On

DATE:16/3/2022

Title- " E-tools for Palgarism & Grammar "
Date: 16/3/2022

Time of event: 10:00 AM

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Plagiarism tools are the modern tools that we use to detect the portions of duplicate or copied content in everything from research journals to site content. They are very quick, exact and can work in numerous languages. A plagiarism detector allows you to check if you correctly identified the literature you used. Therefore, if you are not completely certain whether you quoted a text correctly, you should seriously consider running your document through a plagiarism checker.

Turnitin is a web-based plagiarism prevention system used by most universities in the UK. There are three main uses of Turnitin:

- To act as a deterrent against plagiarism.
- To provide reports which can help identify occurrences of plagiarism.
- To provide students with a tool to identify and correct possible occurrences of plagiarism in their own work and improve their academic writing.

How does Turnitin work?

A Turnitin assignment is set up by a lecturer through a university's online learning environment. Students then access this assignment online and upload their work before the due date. Turnitin will then analyse the submitted work to identify text matches with other sources, usually completing this task within a few minutes. Staff can also view the work that has been submitted and there is also an option to mark the work online and include grades and comments which can be returned to the student once all papers have been marked.

For each piece of submitted work, Turnitin provides two things:

- A *similarity index*, which indicates the percentage of the submitted paper that Turnitin has identified as being matched against other sources.
- An *originality report*, which shows each of these matches in more detail, including the source(s) that Turnitin has found. These can be websites, books, journals and articles, or work that has previously been submitted through Turnitin.
- The most important thing to understand about Turnitin is that it does not directly identify plagiarism; instead it provides a report that allows students and staff to see where plagiarism may have occurred. No student would be accused of plagiarism without a member of staff first reviewing this report in detail to verify that there are indeed grounds for such an accusation based on reasonable academic judgement.
- When you submit work to Turnitin, it is usually stored within the Turnitin database so that it can be cross- checked against future submissions from other UK universities. You retain the copyright and intellectual property of all work that you submit. The makers of Turnitin also work closely with the UK Information Commissioner's Office to ensure that your work is used fairly and legally.
- The Turnitin database is very large and growing. Turnitin has agreements with many major publishers to include books, journal articles and conference proceedings when they are published. There is also an ongoing project to include many older, out-of-print and back-catalogue books and articles. However, Turnitin does not contain every word ever written, and so may miss some matches even where text has been copied from another source.
- Although a high originality index may indicate possible plagiarism and a low originality index may indicate original work, this is not always the case. In many subjects, learning the 'language of the discipline' is part of the university academic process, so it is highly likely – perhaps even desirable – that some matches will occur. The extent of this depends on the nature of the subject, how many quotations you use and your own academic writing style.

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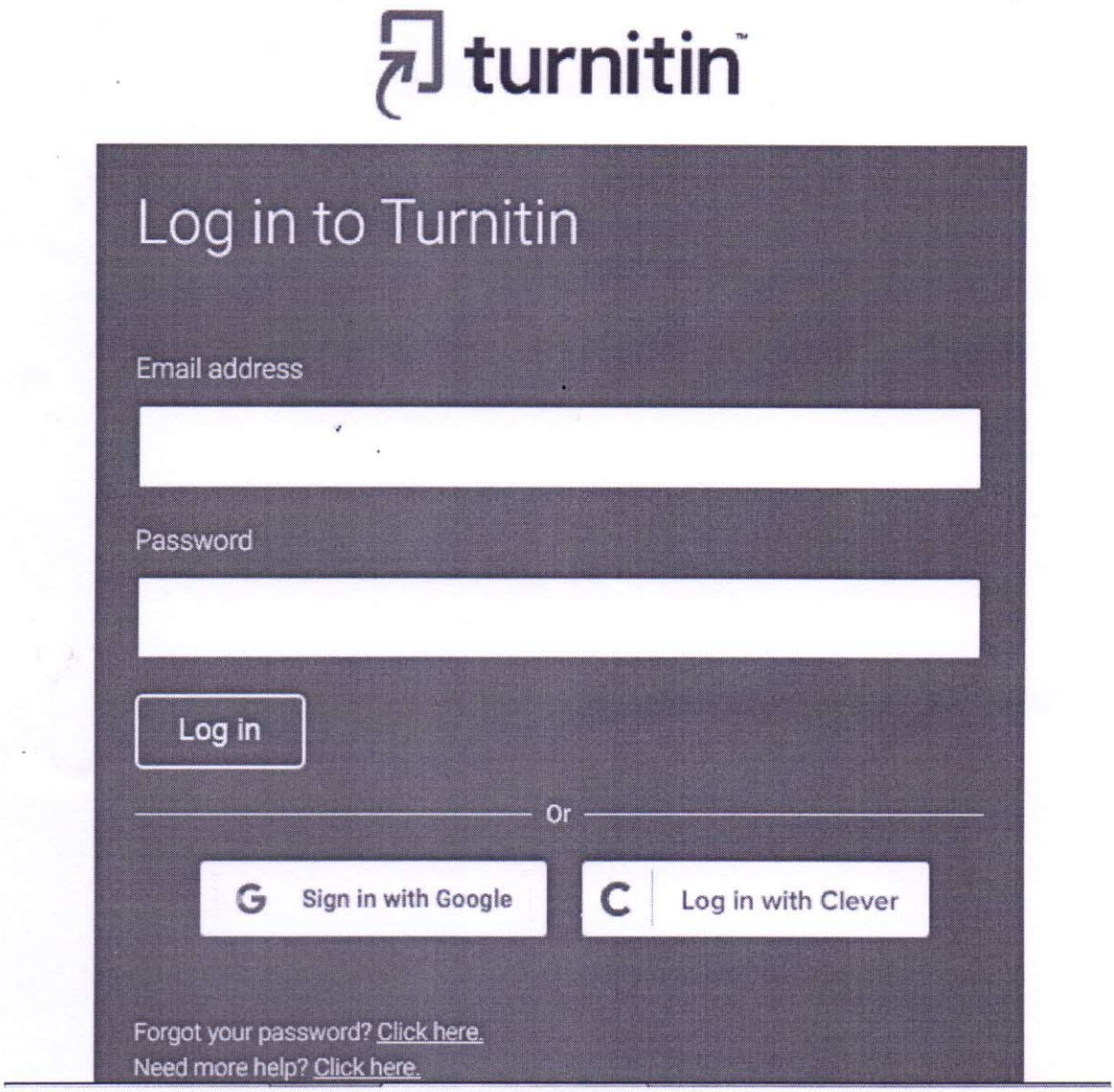
- Do not aim for a specific originality index that you think will be low enough to avoid detection. Accusations of plagiarism can occur even if only a small amount of text is copied. Instead aim for academically sound writing with all your sources properly acknowledged.
- Turnitin is only one of the tools used to identify possible occurrences of plagiarism. Don't forget that your work will also be read by subject experts with years of experience in marking student work. They are able to spot instances of plagiarism even where electronic systems such as Turnitin do not.



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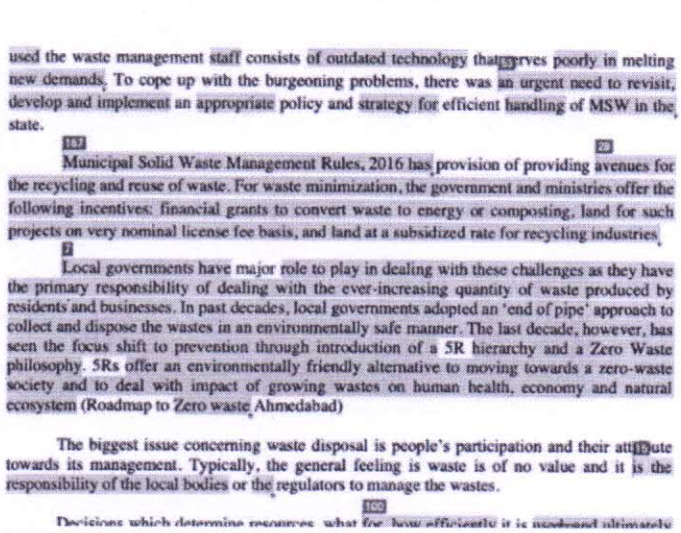
Sample report generated by "Turnitin"



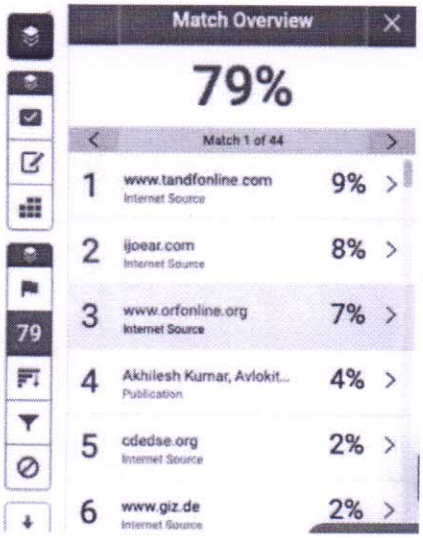
used the waste management staff consists of outdated technology that serves poorly in melting new demands. To cope up with the burgeoning problems, there was an urgent need to revisit, develop and implement an appropriate policy and strategy for efficient handling of MSW in the state.

Municipal Solid Waste Management Rules, 2016 has provision of providing avenues for the recycling and reuse of waste. For waste minimization, the government and ministries offer the following incentives: financial grants to convert waste to energy or composting, land for such projects on very nominal license fee basis, and land at a subsidized rate for recycling industries.

Local governments have major role to play in dealing with these challenges as they have the primary responsibility of dealing with the ever-increasing quantity of waste produced by residents and businesses. In past decades, local governments adopted an 'end of pipe' approach to collect and dispose the wastes in an environmentally safe manner. The last decade, however, has seen the focus shift to prevention through introduction of a 5R hierarchy and a Zero Waste philosophy. 5Rs offer an environmentally friendly alternative to moving towards a zero-waste society and to deal with impact of growing wastes on human health, economy and natural ecosystem (Roadmap to Zero waste Ahmedabad)

The biggest issue concerning waste disposal is people's participation and their attitude towards its management. Typically, the general feeling is waste is of no value and it is the responsibility of the local bodies or the regulators to manage the wastes.

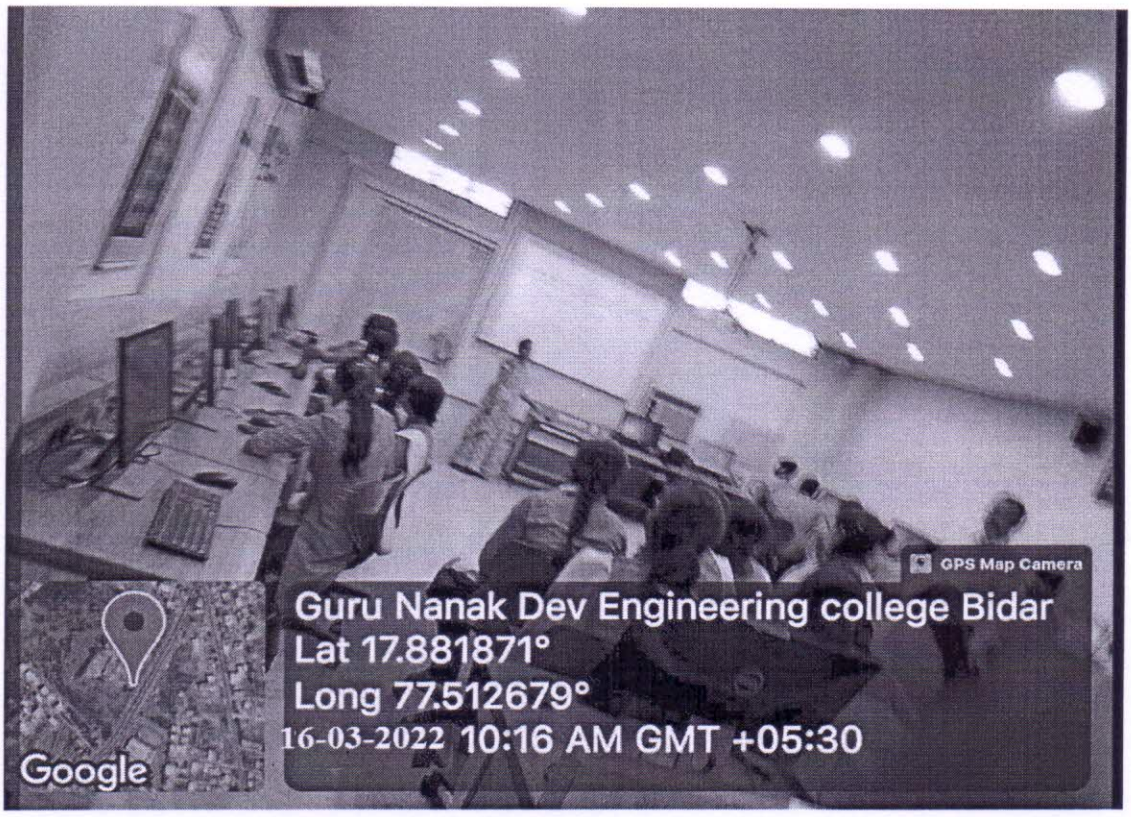
Decisions which determine resources, what for, how efficiently it is needed ultimately



Event Poster.

Glimpse of event: (attach geo tagged photos)

Photo Gallery:



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Report on

"Collaborative writing & editing tools -Latex"

Organized by

"Faculty of Electronics and Communication Engineering"

On

DATE:17/3/2022

Title- " Collaborative writing & editing tools –Latex"
Date: 17/3/2022

Time of event: 10:00 AM

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Collaborative writing tools like Google Docs or Dropbox Paper are becoming more popular among professionals who need to create, edit, and share documents with their teams or clients. These tools offer many benefits, such as real-time collaboration, cloud-based storage, version control, and feedback features.

By working with other writers, you can benefit from their expertise, feedback, and insights, and avoid errors and biases that you might miss on your own. Collaborative writing can also help you generate more ideas, explore different perspectives, and develop critical thinking skills.

Editing involves looking at each sentence carefully, and making sure that it's well designed and serves its purpose. Proofreading involves checking for grammatical and punctuation errors, spelling mistakes, etc. Proofing is the final stage of the writing process.

Editing serves multiple purposes: to fix mistakes, clarify the message, cut down (or build up) text to meet a specified word count, change the writing's tone, make it fit particular constraints, and hone language for an intended audience. Learning how to be a good editor will make you a better writer overall.

Various arguments can be proposed for, or against, learning to use LATEX instead of other document-authoring applications; but, ultimately, it is a personal choice based on preferences, affinities, and documentation requirements.

LATEX

Arguments in favour of LATEX include:

- support for typesetting extremely complex mathematics, tables and technical content for the physical sciences;
- facilities for footnotes, cross-referencing and management of bibliographies;
- ease of producing complicated, or tedious, document elements such as indexes, glossaries, table of contents, lists of figures;
- being highly customizable for bespoke document production due to its intrinsic programmability and extensibility through thousands of free add-on packages.

Overall, LATEX provides users with a great deal of control over the production of documents which are typeset to extremely high standards. Of course, there are types of documents or publications where LATEX doesn't shine, including many "free form" page designs typically found in magazine-type publications.

One important benefit of LATEX is the separation of document content from document style: once you have written the content of your document, its appearance can be changed with ease. Similarly, you can create a LATEX file which defines the layout/style of a particular document type and that file can be used as a *template* to standardise authorship/production of additional documents of that type; for example, this allows scientific publishers to create article templates, in LATEX, which authors use to write papers for submission to journals. Overleaf has a gallery containing thousands of templates, covering an enormous range of document types—everything from scientific articles, reports and books to CVs and presentations. Because these templates define the layout and style of the document, authors need only to open them in Overleaf—creating a new project—and commence writing to add their content.

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```
\documentclass[12pt, letterpaper]{article}
\title{My first LaTeX document}
\author{Hubert Farnsworth\thanks{Funded by the Overleaf team.}}
\date{August 2022}
```

To typeset the title, author and date use the `\maketitle` command within the *body* of the document:

```
\begin{document}
\maketitle
We have now added a title, author and date to our first \LaTeX{} document!
\end{document}
```

The preamble and body can now be combined to produce a complete document which can be opened in Overleaf:

```
\documentclass[12pt, letterpaper]{article}
\title{My first LaTeX document}
\author{Hubert Farnsworth\thanks{Funded by the Overleaf team.}}
\date{August 2022}
\begin{document}
\maketitle
We have now added a title, author and date to our first \LaTeX{} document!
```

This example produces the following output:

My first LaTeX document

Hubert Farnsworth*

August 2022

We have now added a title, author and date to our first LaTeX document!

Event Poster.

Glimpse of event: (attach geo tagged photos)

Photo Gallery:

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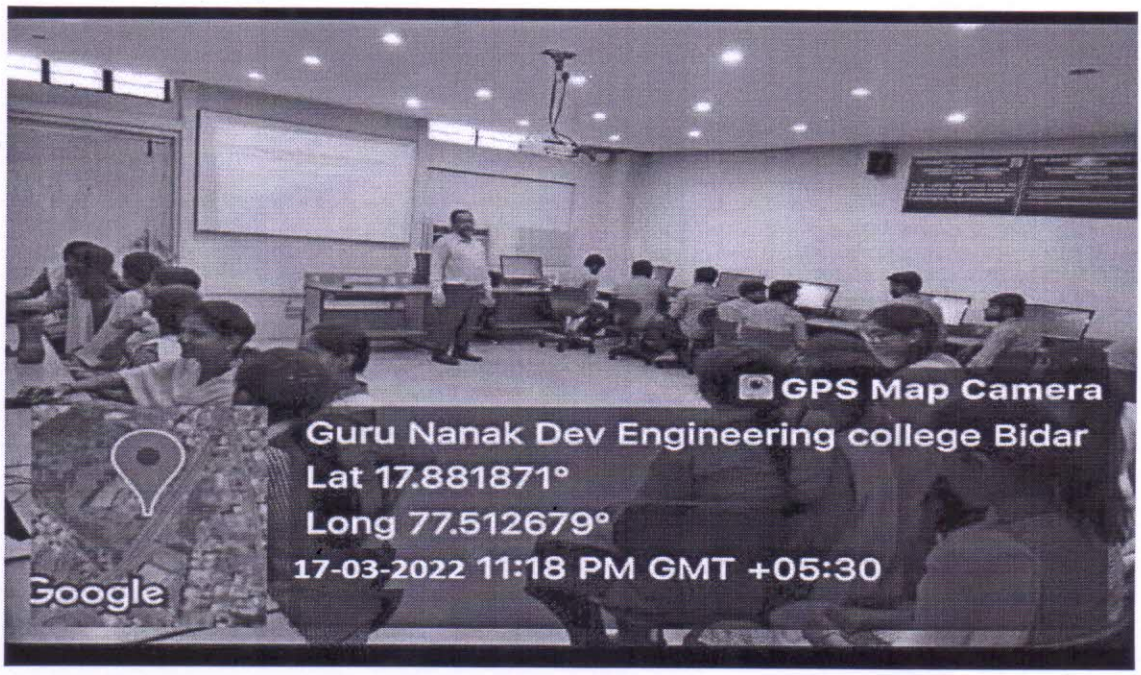

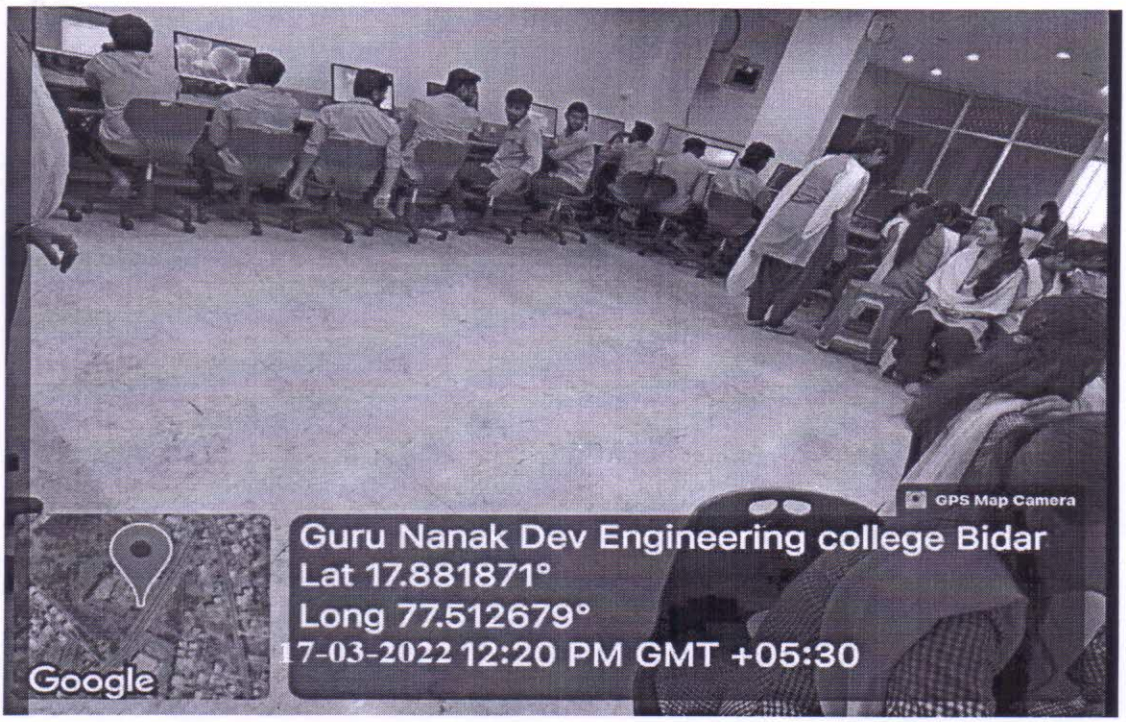

Co-ordinator

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HoD